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REMARKS

Claims 16-29 were pending in the application and all were rejected. Applicant has

canceled claims 16-29 and added new claims 30-33. Support for the new claims can be found in

Applicant's disclosure as published in United States Patent Publication 2005/0060520,

specifically in paragraphs 20-22, and 28-32. Applicant respectfully requests reconsideration.

SPECIFICATION

The Office Action objected to the specification as failing to provide proper antecedent

basis for "computer readable medium" as recited in claim 26.

Claim 26 has been canceled, thus mooting this objection.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The Office Action rejected claims 16-29 under 35 U.S.C. 102(b) as being anticipated by

Agarwal, et al. (US 5,832,533).

New claims 30 - 33 replace claims 16-29. New claim 30 is not anticipated by Agarwal

because Agarwal's method of addressing registers does not provide for encoding ordered tuples

of register specifiers of addressable registers. Specifically, Agarwal does not teach the claim

element of "encoding ordered tuples of register specifiers of addressable registers for specifying

a plurality of fields of an instruction of fixed length, wherein each ordered tuple comprises all the

register specifiers in the instruction."

Further Agarwal does not teach or suggest fetching an instruction specified in a specific

architected instruction width and then expanding that instruction width. Moreover, commonly-

owned Agarwal fails to teach distinguishing instruction processing between compatibility mode

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and extended mode. Agarwal also fails to teach padding register specifiers if the instruction is to

be processed in compatibility mode. Agarwal: col. 8, lines 32 - 34; "if the index addressing

mode has not been selected, the process utilizes the remaining contents of the operand field as a

direct general purpose register address."

For the foregoing reasons, Applicant respectfully requests entry of the amendment and

allowance of the pending claims.

Respectfully submitted,

/Michael J. Buchenhorner/

Michael J. Buchenhorner

Reg. No. 33,162

Date: October 21, 2008

Michael Buchenhorner, P.A. 8540 S.W. 83 Street

Miami, Florida 33143

(305) 273-8007 (voice)

(305) 595-9579 (fax)

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